

Form PTO-1449 (Modified)

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Atty. Docket No.

20002/17479

Serial No.

10/630,164

Applicant

DESAI

Filing Date

July 30, 2003

Group Art Unit

2186

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>SM</i>	A01	5,664,150	09/1997	Isaac et al.			
	A02	5,765,199	06/1998	Chang et al.			
	A03	5,802,577	09/1998	Bhat et al.			
	A04	5,867,162	02/1999	O'Leary et al.			
	A05	5,913,226	06/1999	Sato			
	A06	6,266,744	07/2001	Hughes et al.			
	A07	6,425,060	07/2002	Mounes-Toussi et al.			
	A08	6,438,660	08/2002	Reams			
	A09	6,519,685	02/2003	Chang			
	A10	6,549,990	04/2003	Hughes et al.			
	A11	6,615,322	09/2003	Arimilli et al.			
	A12	6,615,323	09/2003	Petersen et al.			
	A13	6,694,409	02/2004	Chang			
	A14	6,775,748	08/2004	Jamil et al.			
	A15	5,996,049	11/30/99	Arimilli et al.			
	A16	5,943,684	08/1999	Arimilli et al.			
	A17	6,073,211	06/2000	Cheng et al.			
	A18	6,170,040	01/2001	Lee et al.			
	A19	6,345,340	02/2002	Arimilli et al.			
	A20	6,360,301	03/2002	Gaither et al.			
	A21	6,378,048	04/2002	Prudvi et al.			
	A22	6,549,989	04/2003	Arimilli et al.			
	A23	6,574,710	06/2003	Gaither et al.			

EXAMINER

*Shan H. Cho*

DATE CONSIDERED

7/6/06

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>jm</i>	A24	6,629,212	09/2003	Arimilli et al.			
	A25	6,574,710	06/2003	Gaither et al.			
	A26	6,785,774	08/2004	Arimilli et al.			
	A27	6,834,327	12/2004	Lyon			
	A28	6,321,297	11/2001	Shamanna et al.			
	A29	5,369,753	11/1994	Tipley			
	A30	2003/0154352	08/2003	Jamil et al.			
	A31	5,765,199	06/1998	Chang et al.			
	A32	5,802,577	09/1998	Bhat et al.			
	A33	5,867,162	02/1999	O'Leary et al.			
	A34	6,266,744	07/2001	Hughes et al.			
	A35	6,425,060	07/2002	Mounes-Toussi et al.			
	A36	6,519,685	02/2003	Chang			
	A37	6,515,323	09/2003	Petersen et al.			
	A38	6,694,409	02/2004	Chang			
	A39	5,512,874	04/1996	Poston, Travis			
	A40	5,523,703	06/1996	Yamamoto et al.			
	A41	5,686,872	11/1997	Fried et al.			
	A42	5,731,711	03/1998	Gabara, Thaddeus John			
	A43	6,317,839	11/2001	Wells, Robert Marion			
	A44	6,320,406	11/2001	Morgan et al.			
	A45	6,339,344	01/2002	Sakata et al.			
	A46	6,411,146	06/2002	Kuo, James R.			



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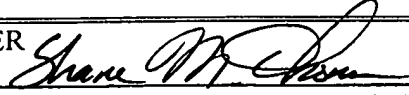
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<i>SM</i>	A47	6,552,578	04/2003	Cheung et al.			
	A48	6,593,801	07/2003	Hattori, Hide			
	A49	2003/0198296	10/2003	Bonelli et al.			
	A50	6,724,891	04/2004	Huang et al.			
	A51	6,791,371	09/2004	Cheung, Hung-Yan			
	A52	5,666,068	09/09/1997	Ehmann			
	A53	6,034,551	03/07/2000	Bridgewater, Jr.			
	A54	6,157,974	12/05/2000	Gasparik			
	A55	6,307,401	10/23/2001	Bridgewater, Jr.			
	A56	2002/0017920	02/14/2002	Bridgewater, Jr.			
	A57	5,959,472	09/28/1999	Nagamatsu et al.			
	A58	5,905,998	05/1999	Ebrahim et al.			
	A59	6,880,031	04/2005	Singh et al.			
	A60	7,000,078	02/2006	Jones et al.			
	A61	6,167,492	12/2000	Keller et al.			
	A62	6,490,661	12/2002	Keller et al.			
	A63	6,405,289	06/2002	Arimilli et al.			
	A64	5,659,710	08/1997	Sherman et al.			
	A65	6,760,819	07/2004	Dhong et al.			
	A66	2005/0166020	07/2005	Jamil et al.			

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	C01	Tomasevic, M; Milutinovic, V. "Hardware Approaches to Cache Coherence in Shared-Memory Multiprocessors, Part 1." IEEE Micro. Vol. 14, Issue 5, pp 52-59. Oct. 1994.
	C02	Tomasevic, M; Milutinovic, V. "Hardware Approaches to Cache Coherence in Shared-Memory Multiprocessors, Part 2." IEEE Micro. Vol. 14, Issue 6, pp 61-66. Dec. 1994.
	C03	"Intel® Itanium™ Processor Hardware Developer's Manual." ftp://download.intel.com/design/Itanium/Downloads/24870102.pdf. Document Number: 248701-002. August 2001.
	C04	"Intel® Itanium™ 2 Processor Hardware Developer's Manual." ftp://download.intel.com/design/Itanium2/manuals/25110901.pdf. Document Number: 251109-001. July 2002.
	C05	"Intel® Pentium® 4 Processor with 512-KB L2 Cache on 0.13 Micron Process Datasheet." ftp://download.intel.com/design/Pentium4/datashts/29864309.pdf. Document Number: 298643-009. May 2003.
	C06	Handy, <i>The Cache Memory Book</i> , Academic Press, Inc., 2 <sup>nd</sup> edition, pp. 126-7; 156-186 (© 1998).
	C07	Lee, <i>A Secondary Cache Controller Design for a High-End Microprocessor</i> , Institute of Electrical and Electronics Engineers, Inc., pp. 1141-1146 (© 1992).

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